

# A Logarithmic Amplifier With Limiter Output 5 Mhz 500 Mhz

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### A Logarithmic Amplifier With Limiter

#### **a Logarithmic Amplifier with Limiter Output 5 MHz-500 MHz ...**

The AD8309 is a complete IF limiting amplifier, providing both an accurate logarithmic (decibel) measure of the input signal (the RSSI function) over a dynamic range of 100 dB, and a programmable limiter output, useful from 5 MHz to 500 MHz It is easy to use, requiring few external components A single

#### **a Logarithmic Amplifier with Limiter Output 50 MHz, 80 dB ...**

the logarithmic amplifier, respectively These parameters are inherent in the design of a particular logarithmic amplifier, although may be adjustable, as in the AD606 When  $V_{IN} = V_X$ , the logarithmic argument is one, hence the logarithm is zero  $V_X$  is, therefore, called the logarithmic intercept voltage because the output voltage  $V$

#### **A Logarithmic Amplifier With Limiter Output 5 Mhz 500 Mhz**

A demodulating logarithmic amplifier with limiter output with 50MHz typical input and 80dB dynamic range is useful over a wide range of signal and power measurements, for ultrasound and sonar processing, and as a phase-stable limiting amplifier to 100 MHz A precision logarithmic and log ratio amplifier is used in

#### **DESIGN OF A 120dB PSEUDO-LOGARITHMIC AMPLIFIER ...**

DESIGN OF A 120dB PSEUDO-LOGARITHMIC AMPLIFIER IMPLEMENTED IN THE AMI 05 $\mu$ m PROCESS By Zachary Richards Advisor: Dr David E Kotecki An Abstract of the Design Project Completed for ECE547: VLSI Design and Layout May 2007 This paper discusses several design methodologies for achieving logarithmic

#### **AD606 50MHz, 80dB Demodulating Logarithmic Amplifier with ...**

the logarithmic amplifier, respectively These parameters are inherent in the design of a particular logarithmic amplifier, although may be adjustable, as in the AD606 When  $V_{IN} = V_X$ , the logarithmic argument is one, hence the logarithm is zero  $V_X$  is, therefore, called the logarithmic intercept voltage because the output voltage  $V$

#### **a Low Cost DC-500 MHz, 92 dB Logarithmic Amplifier AD8307**

Each of the cascaded amplifier/limiter cells has a small-signal gain of 143 dB, with a -3 dB bandwidth of 900 MHz The input is fully differential and at a moderately high impedance (11 k $\Omega$  in parallel with about 14 pF) The AD8307 provides a basic dynamic range extending from approximately -75 dBm

#### **DESIGN AND FABRICATION OF A DETECTOR LOGARITHMIC ...**

logarithmic amplifier The design is composed of two main parts: The Schottky diode detector rectifies the incoming RF signal and produces a video voltage and the logarithmic amplifier transforms the scale of the video voltage from linear scale to logarithmic scale in order to observe the RF signals with a wide amplitude range

#### **A Fast RSSI using Novel Logarithmic Gain Amplifiers for ...**

24 SUNGHO LEE et al : A FAST RSSI USING NOVEL LOGARITHMIC GAIN AMPLIFIERS FOR WIRELESS COMMUNICATION operation [4,5] There are several types of variable gain amplifier (VGA) that have logarithmic transfer characteristics The most conventional logarithmic amplifier uses a weak inversion region in input transistors to achieve

#### **AN-311 Theory and Applications of Logarithmic Amplifiers**

Theory and Applications of Logarithmic Amplifiers www.ti.com 1 Theory and Applications of Logarithmic Amplifiers The theory and construction of these circuits are actually readily understood Figure 1 shows an amplifier that provides a logarithmic output for a linear input current or ...

#### **SA605 High performance low power mixer FM IF system**

Temperature-compensated logarithmic Received Signal Strength Indicator (RSSI) with a dynamic range in excess of 90 dB LIMITER\_OUT 11 limiter amplifier output LIMITER\_DECOUPL 12 limiter amplifier decoupling pin LIMITER\_DECOUPL 13 limiter amplifier decoupling pin LIMITER\_IN 14 limiter ...

#### **Ian Williams TI Precision Designs: Verified Design Active ...**

Ian Williams TI Precision Designs: Verified Design Active Volume Control for Professional Audio logarithmic (or audio) taper, and reverse logarithmic (or reverse audio) taper which is configured as an inverting amplifier with gain  $G$  Therefore, the output voltage  $V_{OUT}$  must equal the product of  $V_X$

#### **AD8307 Low Cost DC—500 MHz, 92 dB Logarithmic Amplifier ...**

The AD8307 is the first logarithmic amplifier in an 8-lead (SOIC-8) package It is a complete 500 MHz monolithic demodulating logarithmic amplifier based on the progressive Each of the cascaded amplifier/limiter cells has a small-signal gain of 143 dB, with a -3 dB bandwidth of 900 MHz The

#### **Log amplifier fundamentals - Signal Pro**

The limiter output sum can be called the characteristic, and the output of the linear limited terms the mantissa The logarithmic transformation: To understand how the amplifier produces a logarithmic output, let us assume that in the chain of amplifiers, 3 amplifiers (the last three) a ...

#### **Low Power Mixer/Limiter/RSSI 3 V Receiver IF Subsystem AD608**

mance is typically  $\pm 1$  dB The logarithmic slope is proportional to the supply voltage A feedback loop automatically nulls the input offset of the first stage down to the submicrovolt level The AD608 limiter output provides a hard-limited signal output at 400 mV p-p The voltage gain of the limiting

amplifier to this output is more than 100 dB

#### **AD8307 Low Cost DC-500 MHz, 92 dB Logarithmic Amplifier ...**

Low Cost DC-500 MHz, 92 dB Logarithmic Amplifier AD8307 The AD8307 is the first logarithmic amplifier made available in an 8-lead (SOIC-8) package It is a complete 500 MHz monolithic Each of the cascaded amplifier/limiter cells has a small signal gain of ...

#### **SA639 Low voltage mixer FM IF system with filter amplifier ...**

92 dB of IF amplifier/limiter power gain 25 MHz limiter small signal bandwidth Temperature compensated logarithmic Received Signal Strength Indicator (RSSI) with a dynamic range in excess of 80 dB RSSI output internal op amp Post detection amplifier for filtering Programmable data switch SA639

#### **AD8310 Fast, Voltage-Out DC-440 MHz, 95 dB Logarithmic ...**

Fast, Voltage-Out DC-440 MHz, 95 dB Logarithmic Amplifier AD8310 Rev E logarithmic amplifier (log amp) with a very fast voltage mode output, capable of driving up to 25 mA into a grounded load in Each of the six cascaded amplifier/limiter cells has a small-signal gain of ...

#### **A CRYSTAL LIMITER FOR USE IN AN FM RECEIVER IN THE ...**

2 Logarithmic Limiter As presently used in the laboratory, the logarithmic limiter consists of two 1N34 germanium crystals paralleled with reversed polarities and placed at the grid of each amplifier tube in the receiver as indicated in fig 2 B+ Figure 2 Position of logarithmic limiter

#### **AD8310 Fast, Voltage-Out DC-440 MHz, 95 dB Logarithmic ...**

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#### **a Low Power Mixer/Limiter/RSSI 3 V Receiver IF Subsystem ...**

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